

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Joyner et al. Docket No.: TI-29912
Serial No: TBD Examiner: TBD
Filed: 02/05/02 Art Unit: TBD
For: METHOD FOR MANUFACTURING AND STRUCTURE FOR
TRANSISTORS WITH REDUCED GATE TO CONTACT SPACING

*Sub 2/9
6-28-02*

PRELIMINARY AMENDMENT

February 5, 2002

Assistant Commissioner for Patents

Washington, DC 20231

Dear Sir:

Please amend the above referenced application as follows:

In the Specification:

Page 1, before line 1, insert This application claims priority under 35 USC §

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119(e)(1) of provisional application numbers 60/266,899 filed 02/06/01.--